

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,061,213 B2
APPLICATION NO. : 10/613017
DATED : June 13, 2006
INVENTOR(S) : Yutaka Yoshida

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Please change column 12, line 10, “out ud” to -- out_ud--,

column 15, line 55, “FIG. 12(d)” to -- FIG. 12(d). --,

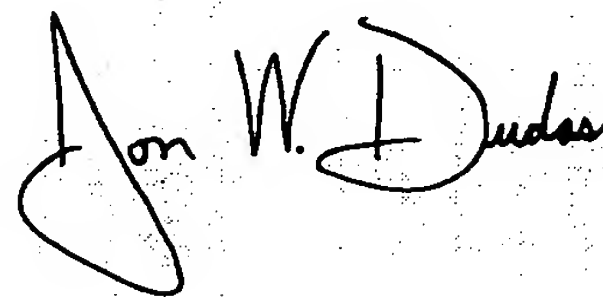
column 23, line 61, “Verr<Vr6” to -- Verr≤Vr6 --,

column 24, line 3, “PFM” to -- PWM --, and

column 25, line 56, “output-signal” to -- output signal --.

Signed and Sealed this

Fifth Day of September, 2006

A handwritten signature in black ink, reading "Jon W. Dudas". The signature is stylized, with the first name "Jon" and last name "Dudas" clearly legible, and "W." in the middle.

JON W. DUDAS
Director of the United States Patent and Trademark Office